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High Performance Amplifier Solutions for LTE and Beyond





RAN Evolution Challenges, coverage and capacity



- High data rates in wireless networks will reach theoretical limits (Shannon Theorem)
 - Necessitates better interference management, more complex equipment
- MIMO
 - Increased demand of processing power
 - Increased antenna, antenna networks, site clutter (2X2, 4X4, Active Antenna)
- OFDM
 - Increased heat dissipation, High Peak/Average ratios
 - Higher frequencies (2.5, 3.5 GHz, etc...)
- In-building is extremely important (50% users are indoor)
 - 2X data rate = 50 % more Base stations!
 - Location of terminals determines BTS density
- Larger BWs of Operation
- Extreme pressure in both commercial and military communications for lower pricing, Lower OPEX





Markets for Communication Amplifiers





Military Market Drivers

- Military Communications
 - SDR Inter-operability, multi-standard, high BW
 - Portable and mobile platforms
 - Integrated Datalinks (voice/data/video)
 - COTS, lower cost

Military Comms Market Size: 15.2 Billion

(asdreports.com)



BTS Market Drivers

- Lower Cost, High Reliability
- Remote Radio Heads
 - eliminate Losses
 - DAS In/building systems
- Improved BTS efficiency
 - reduced OpEx costs

•Mobile Telecom Market Size: 40 B (reuters.com)







Market Segments





•Market Size by Revenue

•EW = \$37M (4%)

•Radar = \$130M (14%)

•Other Comms = \$87M (9%)

•ISM = \$50M (5%)

•Wireless Infrastructure = \$550M (58%)

•Broadcast = \$60M (6%)

- •Other = \$36M (4%)
- •
- Total = \$950M

Source: ABI



WSE: Advancements in Linear Power Amplifiers for Cellular Infrastructure

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Power Transistor Market Analysis













Need for Efficiency



•Operators target 50% reduction in their power consumption by 2020 •Improving the •Scaling of energy Increase overall •QoS/RF power needs with traffic base station efficiency ratio Increased PA efficiency •The remaining of the •will substantially targeted 50% reduction contribute to power



consumption reduction



Design Approach





System level:

[©] Single or multi-branch architectures: Doherty, drain modulation, predistortion

Circuit level:

^CPA class of operation: A, AB, B, C, F, E, S...

Device level:

^CTransistor technology: GaN, LDMOS, HBT etc...





Amplifier Architectural Roadmap









Highly Efficient Transmitters

•Doherty PA

•Envelope Tracking









Large input BW (60 MHz)
RF in/out

Large linearization BW (300 MHz)

Low-Moderate distortion suppression (> 8-20 dB)

Potential for high system efficiency (25-30%, or higher)

Low cost

Lower complexity than DPD

A good choice for applications in which the PAM is not highly nonlinear and exhibits low memory effects

Low Power consumption



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•APD Basic Block Diagram







Digital Predistortion (DPD)



Moderate input BW (20 MHz)

Moderate linearization BW (100 MHz)

Potential for large distortion suppression (> 25 dB)

Potential for moderate system efficiency (20-25%)

Better linearization performance at the expense of higher complexity and cost

 For some applications the only solution that combines adequate correction and signal BW









• Pout = 47 dBm. F = 761 MHz. PAM Efficiency = 46%



APD and DPD IMD suppressions at this power were similar (about 1 dB difference) •Slide 13





Typical Radio Head Efficiency Budget

#TX/RX paths			RF drivers (W)	PA out rms (W)	PA isol etc. (dB)	filter (dB)	Out (W/path)
2			8	24.9	-0.35	-0.6	20
DC/DC efficiency (%)		Freq (MHz)	PAPR (dB)	Final stage PA eff (%)			RF out (W)
89%		2100	7.0	50%			40
General Interface control power (W)	linearization (W/path)	LNA/VSW R (W/path)				power consumed (W/path)	total power consumed (W)
15	12.5	2		49.8		81	179
	-	RH					



2TX/2RX RRH 20W per TX

RH efficiency (%)	22.3%		
Power dissipated (W)	139		



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Typical Radio Head Efficiency Budget

	-			-		-	
#TX/RX paths			RF drivers (W)	PA out rms (W)	PA isol etc. (dB)	filter (dB)	Out (W/path)
2			8	24.9	-0.35	-0.6	20
DC/DC efficiency (%)		Freq (MHz)	PAPR (dB)	Final stage PA eff (%)			RF out (W)
89%		2100	7.0	60%			40
General Interface control power (W)	linearization (W/path)	LNA/VSW R (W/path)				power consumed (W/path)	total power consumed (W)
15	12.5	2		41.5		72	161
					•	RH	



2TX/2RX RRH 20W per TX





•IMPROVED PA EFFICIENCY

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Typical Radio Head Efficiency Budget

#TX/RX paths			RF drivers (W)	PA out rms (W)	PA isol etc. (dB)	filter (dB)	Out (W/path)
2			8	24.9	-0.35	-0.4	20
DC/DC efficiency (%)		Freq (MHz)	PAPR (dB)	Final stage PA eff (%)			RF out (W)
89%		2100	7.0	50%			40
General Interface control power (W)	linearization (W/path)	LNA/VSW R (W/path)				power consumed (W/path)	total power consumed (W)
8	10	2		47.5		76	161
	RH efficiency (%) Power	25%					
•IMPRO\	(W)	121					





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Efficiency is not everything









•Reliable, Low Cost, High Performance









Performance summary 700 MHz HE PA- LTE

Pout (dBm)	Signal	PA R (dB)	Correctio n (dB)	Eff (%)	Corrected Linearity (dBc) ACLR1/ACL R2	* RBW 30 kHz * VBW 300 kHz * VBW 4
46	010 LTE Fc = 761 MHz	8	20	33	-51.55/ 58.81	2 RM -20 VIEW -20 -30 -30 -50 -50 Center 761 MHz 3.6 MHz/ Span 36 MHz
47	010 LTE Fc = 761 MHz	8	22	37	-51.22/ -57.73	Tx ChannelExtBandwidth5 MHzPower46.11 dBmAdjacent ChannelLower-51.55 dBBandwidth5 MHzUpper-53.27 dB
48	010 LTE Fc = 761 MHz	6.5	23	40	-52.11/ -58.15	Alternate ChannelLower-59.38 dBBandwidth5 MHzLower-59.38 dBSpacing12 MHzUpper-58.81 dB

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Summary



Cost, efficiency, reliability are key to both commercial and military communications markets

Wide Bandwidths, multiple standards operation are driving more challenging designs

+ Compact size and portability are key in tactical radios

Advanced Spread Spectrum techniques will only make matters more difficult

+ Amplifiers are moving to the antenna

